Appl. No. 10/009,910 Amdt. Dated September 22, 2004 Reply to Office Action of June 24, 2004

Attorney Docket No. 81839.0107 Customer No. 26021 CENTRAL RY: CRITTER

SEP 2 2 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Makoto IIDA et al.

Serial No.: 10/009,910

Confirmation No.: 7347

Filed:

December 12, 2001

For:

SILICON WAFER, SILICON

EPITAXIAL WAFER, ANNEAL

WAFER AND METHOD FOR

PRODUCING THEM

Art Unit: 1765

Examiner: Matthew J. Song

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop AF

Commissioner for Patents

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P.O. Box 1450 Alexandria, VA 22313-1450

September 22, 2004 Date of Deposit

John P. Schedaetter, Reg. No. 23,009

Date

RESPONSE TO FINAL OFFICE ACTION

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Commissioner for Patents

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Dear Sir:

I hereby cartify that this paper and every paper referred to herein is being transmitted via facsimile to recipient at (703) 872-9308 on: <u>September 22, 2004</u>

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In response to the Final Office Action of June 24, 2004, please enter and consider the following Remarks/Arguments:

The Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.

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